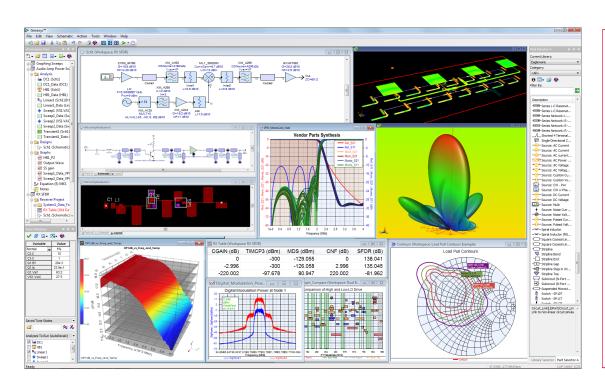
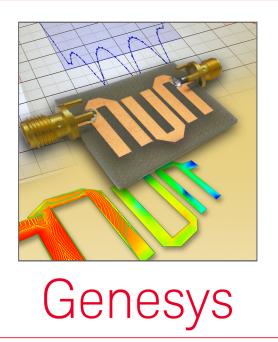
# Keysight Technologies

# Genesys

An integrated simulation and synthesis design tool for RF/microwave circuit board and subsystem designers







### Introduction



Genesys

Keysight Genesys is an affordable, accurate, easy-to-use RF and microwave circuit synthesis and simulation tool created for the circuit board and subsystem designer. Providing the optimal balance of capabilities with ease-of-use, designers can quickly attain the skills necessary to operate the tool while realizing unbeatable engineering productivity through powerful automatic circuit synthesis technology. Genesys is available in 6 different languages (English, Japanese, Korean, Chinese simplified, Chinese traditional and Russian), further adding to its ease of global deployment and collaboration by engineers and technicians.

Genesys is endorsed by an installed base of over 5,000 satisfied RF and microwave designers worldwide, many of whom have been loyal repeat customers over the past 30 years. Genesys incorporates breakthrough nonlinear X-parameter simulation and is backed by Keysight's extensive industry-wide expertise in RF/microwave design, instrumentation and support. As a proven safe investment, Genesys literally pays for itself through cost savings within its first year of deployment as a design productivity tool. As your requirements expand to include enterprise level design of RF/high speed boards, MMICs or multi-technology RF system-in-package (SIP) modules, Keysight Technologies, Inc. protects your Genesys investment by providing full trade-up credit towards the even more capable Advanced Design System (ADS).

The core capabilities of Genesys can be extended with additional simulation and synthesis building blocks into powerful and affordable bundles.

#### Genesys configuration overview

Genesys offers the highest design productivity by providing:

- Industry's widest coverage of RF and microwave automatic circuit synthesis
- Comprehensive RF system architecture and frequency planning tools
- Modulated RF analysis of circuits and systems for EVM, BER and ACPR with WLAN 802.11ac and LTE-3GPP verification
- Time- and frequency-domain circuit simulation with optimization
- Fast, memory-efficient 3D-planar electromagnetic (EM) simulation
- Accurate and convenient X-parameter nonlinear circuit and system simulation
- Accurate frequency, temperature and bias dependent Sys-parameters models of vendor system blocks

#### Genesys core environment

All Genesys configurations start with the prerequisite Genesys core environment, which is itself a full-featured design bundle. Extended capabilities are added to other affordable bundles to include:

- Filter, matching and circuit synthesis
- RF system architecture
- Modulated RF analysis (Dataflow- and frequency-domains)
- Nonlinear circuit simulation (DC, time- and frequency-domains)
- 3D-planar EM simulation

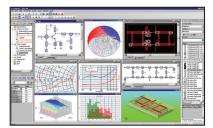
#### Key building blocks

Genes	Genesys bundles are comprised of one or more of these building blocks.					
	Core	Schematic, Layout, Plot, Linear Analysis, Optimization, Instrument Links				
	Filter and match	Filter & Matching Circuit synthesis				
	Synthesis	Custom Filter, Mixer, Oscillator, Signal Control, Equalizer Vendor Parts synthesis				
	System	System simulation, Budget analysis & Frequency Planning synthesis				
	Modulated RF	RF Modulated RF analysis of EVM, BER and ACPR with WLAN & LTE verification				
	Circuit	Harmonic Balance & SPICE circuit simulation				
	EM	3D Planar Electromagnetic simulation of printed circuits and antennas				

# Core Building Block

#### Capability module

W1320BP/BT Genesys core



#### Description

Design environment

- Integrated, easy-to-use Windows-based graphical user environment for hierarchical design creation and management; available in six major languages worldwide
- Scripting from Matlab script, Visual Basic, C++, VBScript or JScript for automation of Genesys to perform custom or repetitive tasks
- LiveReport for creating auto-updating and interactive design documentation
- Export Genesys designs for use in Keysight ADS in design collaboration

Fast linear simulation and powerful optimization

- Interactive tuning for quick insights
- Fast and robust design optimizer
- Linearizes nonlinear components at their DC bias before linear simulation.

NOTE: Full DC analysis requires Harbec/Cayenne

#### www.keysight.com/find/eesof-genesys-core

Statistical simulation

- Monte Carlo yield analysis
- Graphical and spreadsheet report

Data manipulation and display

- Data sets for persistent storage of simulation and measurement data for post-processing and display to eliminate wasteful re-simulation
- Matlab scripting with 100% compatibility with Matlab for custom equations, functions and data processing
- Flexible data display and analysis with rectangular, polar, Y/Z Smith, histogram, 3D-parametric plots, and instrument-style marker readouts
- Interactive 3D viewer for EM surface currents and antenna far-field patterns

RF/microwave layout and artwork translators

- Create layout from schematic, imported artwork, or direct drawing for EM simulation and board fabrication
- 3D viewer for layout with interactive rotation, zoom, vertical stretching, and cut planes to verify correct geometry before fabrication
- Export Python script of layout, materials and ports to Keysight EMPro for full 3D EM simulation
- Full library of pad/package layout footprints
- Import /export masks and drill files in popular printed circuit board (PCB) formats (e.g., Gerber, DXF/DWG, and GDSII) for PCB board realization on fast prototyping machines or chemical etching

Libraries of simulation models and parts

- Full libraries of accurate high-frequency physical models with automatic discontinuities
- Over 30,000 linear, nonlinear and system parts libraries

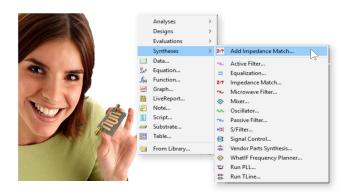
Testlink

- Captures measured data directly into Genesys for simulation and display on network analyzers, impedance analyzers, oscilloscopes, vector/spectrum analyzers, semi-conductor analyzers, and power meters
- Supports over 140 instruments from more than 14 equipment manufacturers

www.keysight.com/find/eesof-genesys-testlink

# Genesys Synthesis

Genesys provides the industry's widest coverage of passive and active circuit synthesis capabilities. The synthesis modules create high-performance circuits, accelerate routine design tasks from hours to minutes, and enable fast make-or-buy decisions on RF components. All 12 synthesis modules are included in the Genesys Synthesis building block. An economical subset containing the 4 most popular synthesis modules is the "filter and match" building block.



Improve productivity with the industry's widest coverage of RF and microwave circuit synthesis capabilities in Genesys.

#### Filter and Match building block

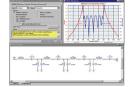
#### Capability module

#### Description

#### Filter synthesis

Classical lumped filter synthesis for RF applications. This module is the single highest-selling synthesis module from our Genesys product line.

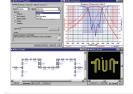
#### www.keysight.com/find/eesof-genesys-filter



#### M/filter synthesis

Distributed filter synthesis for microwave applications with over 60 topologies, including automatic layout for subsequent EM analysis. Synthesizes high-performance microwave filters and assists make-versus-buy decisions.

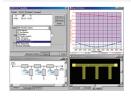
#### www.keysight.com/find/eesof-genesys-m-filter



#### Match synthesis

Synthesizes impedance-matching networks over narrow/broad frequency bands with lumped/distributed components and complex frequency-dependent loads. Creates simultaneous input, output and inter-stage matching for cascaded non-unilateral devices.

#### www.keysight.com/find/eesof-genesys-match



#### Advanced transmission line synthesis

Synthesizes 13 types of transmission lines with lump-distributed circuit conversion and automatic discontinuity insertion. Converts ideal electrical designs to physical implementation such as microstrips and striplines on your choice of substrate.

#### www.keysight.com/find/eesof-genesys-advanced-t-line



# Synthesis – Includes Filter and Match

#### Capability module

#### Description

#### S/filter synthesis

Direct synthesis technology enables precise shaping of filter response with transmission zeros to automatically create custom lumped and distributed filter topologies. Comes with over 200 interactive lumped and distributed topological transforms for high-performance custom filter realization.

#### www.keysight.com/find/eesof-genesys-s-filter

#### A/filter synthesis

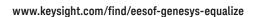
Active op-amp filter synthesis with over 30 active topologies. Use for IF, video, baseband frequencies, and control applications such as power control and AGC circuits.

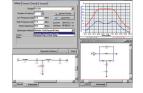
#### www.keysight.com/find/eesof-genesys-a-filter



#### Equalize synthesis

Synthesizes equalization networks to compensate for linear phase distortions in the circuit or system that impacts modulation fidelity such as error vector magnitude (EVM), video, and audio fidelity.

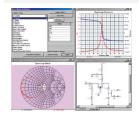




#### Oscillator synthesis

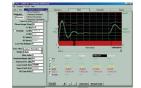
Explore 19 RF oscillator topologies from classical L-C, transmission line, SAW, crystal, cavity, and coaxial hybrid. Recommended companion to the Harbec or Cayenne circuit simulators.

#### www.keysight.com/find/eesof-genesys-oscillator



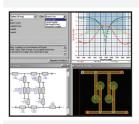
#### PLL synthesis

Phase-locked loop (PLL) synthesis of analog loop filters and 5 setup wizards to design frequency synthesizers and phase/frequency modulators/demodulators.



www.keysight.com/find/eesof-genesys-pll

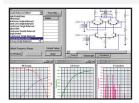
# Synthesis - Includes Filter and Match continued



#### Signal control synthesis

Synthesizes a variety of lumped and distributed couplers (10 types), splitters (10 types), Baluns (5 types), and attenuator (2 types) circuits that control RF signal flow.

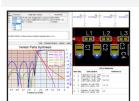
www.keysight.com/find/eesof-genesys-signal-control



#### Mixer synthesis

Explore a range of performance trade-offs between 11 RF mixer topologies based on BJTs, FETs and diodes from diode rings to Gilbert cells. Design companion to the Harbec circuit simulator.

www.keysight.com/find/eesof-genesys-mixer



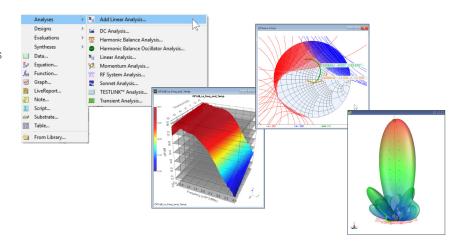
#### **Vendor Parts synthesis**

Automatically replace real-valued RLC parts in any design with purchasable discrete vendor RLC with accurate parasitic models from Modelithics and performs a grid optimization of available values so that your actual RF-PCB meets original specs.

www.keysight.com/find/eesof-genesys-vps

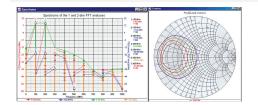
# Genesys Simulation

Genesys offers comprehensive circuit, system and electromagnetic simulation capabilities that are provided in the following 4 building blocks which are used to construct powerful and economical Genesys RF and microwave board design bundles.



#### Circuit Building Block

#### Capability module

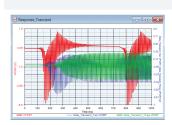


#### Description

#### Harbec

Harmonic balance, nonlinear frequency-domain circuit simulation and optimization that calculates harmonic spectrum at any circuit node, IP3, compression, efficiency, conversion gain, phase noise, load pull contour, large-signal oscillator, amplifier, or mixer. Indispensable tool for RF/microwave and DC bias designs with active transistors, diodes, and components. Harbec now incorporates Keysight's breakthrough nonlinear X-parameters simulation technology for convenient and accurate nonlinear circuit designs with X-parameter models of transistors and RFICs.

www.keysight.com/find/eesof-genesys-harbec



#### Cayenne

Spice simulation for RF circuits that works from the same schematic and RF physical models as Harbec. Includes convolution algorithm to use S-parameters and frequency-domain transmission-line models in accurate time-domain transient simulations of high-speed signal paths. Includes full DC analysis and optimization of DC voltages and currents

www.keysight.com/find/eesof-genesys-cayenne

# Genesys System Architecture

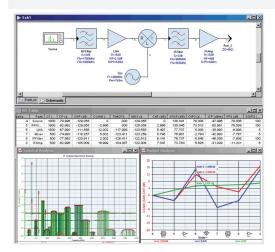
The Genesys system building block includes Spectrasys, a powerful RF system architecture and diagnostic simulator, and WhatIF, an innovative frequency planning synthesis that instantly identifies spurious-free bands when designing frequency conversion systems.

#### System building block

# 660 1100

Simulate and build RF systems with real parts

#### Capability module





 $\ensuremath{\mathsf{X-Microwave}}.\ensuremath{\mathsf{com}}$  RF system prototyping with  $\ensuremath{\mathsf{X-parameters}}$  system blocks

#### Description

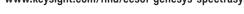
#### **Spectrasys**

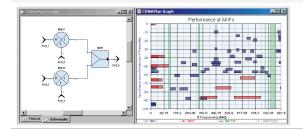
RF system architecture design with accurate behavioral and data-based system blocks to completely analyze system block diagram performance. Unique root-cause diagnostics identify culprit components that impair system performance such as origins of nonlinear spurious mixing intermods ignored by traditional spreadsheet calculations. Especially useful for establishing confidence in system design reviews before hardware realization to avoid costly implementation mistakes downstream.

Spectrasys uses Keysight Sys-Parameters which are frequency, temperature and bias dependent nonlinear system component models that directly use datasheet specs supplied by component vendors such as Mini Circuits and Analog Devices. You can also create spreadsheets of P1dB, IP3, IP2, NF, etc. for use by these models to accurately simulate off-the-shelf components in your system before purchase. X-parameters from nonlinear network measurements or ADS circuit simulation are also convenient and accurate models of system block s used in Spectrasys. X-parameter libraries representing popular system blocks from ADI, MiniCircuits, MACOM, DLI, IDT, Custom MMIC and others are downloadable from www.xmicrowave.com for rapid system proptotyping.

DC power estimator summarizes the different voltage and current drain required by each system component to enable the proper sizing of the system power supply. This is an important aspect of RF system design that other tools neglect.

www.keysight.com/find/eesof-genesys-spectrasys





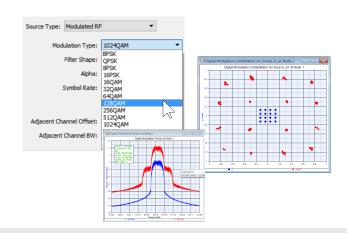
#### WhatIF

Unique, graphical frequency-planning synthesis tool that quickly identifies spurious-free bands across a wide bandwidth involving multiband conversions to a common IF, using realistic mixers. Useful for designing multiband down-converters with high- and low-side LO's. A natural companion to Spectrasys RF system architecture design tool.

www.keysight.com/find/eesof-genesys-whatif

# Genesys Modulated RF

The Genesys Modulated RF capability extends the prerequisite System building block by adding the SystemVue dataflow simulator. It enables RF designers to easily specify digitally modulated RF signals to analyze circuits and systems for digital modulation metrics such as EVM, BER and ACPR. Included WLAN and LTE verification libraries assure design compliance with the latest wireless standards.



#### Modulated RF building block

#### Capability module

# SystemVue

#### Description

#### SystemVue dataflow simulator

The SystemVue dataflow simulator enables digitally modulated RF signals to be used for simulating circuits and systems to calculate digital RF figure of merits such as EVM and ACPR.

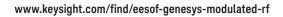
It comes with ver 30 digital modulation schemes, selectable through a pull down menu on the signal source. Once the digital modulation source is selected, all dataflow parameters, including data sinks are automatically set up correctly for the analysis; thus relieving the RF engineer from having to deal with unfamiliar digital signal processing (DSP) dataflow parameters before starting the simulation.

#### www.keysight.com/find/eesof-systemvue-engine

#### Modulated RF budget analysis

The modulated RF budget analysis of EVM, BER and ACPR on the level diagram is a breakthrough capability in the industry that enables the RF system designer to pinpoint which components in the system architecture are contributing to EVM, BER or ACPR failures.

Unlike a single pass/fail simulation, the level diagram indicates stage-by-stage the relative contribution of each component in the system chain to the overall system performance. Based on patented fast estimation algorithms, modulated RF budget analysis also allows interactive tuning of individual component specs to see their relative impact on EVM, BER or ACPR during design.



# System Data Path EVNE EVM | EVM | Intermods | Internods | Intermods | Internods | Intermods | Intermods | Intermods | Internods | Interno

#### Verification library LTE-3GPP and WLAN 802.11ac

Instead of going through volumes of LTE-3GPP or WLAN802.11ac test and compliance specifications in trying to set up complicated simulations for verifying your circuit or system design, Genesys has already done that for you.

The included LTE-3GPP and WLAN 802.11ac verification libraries comes ready set up with correct default dataflow parameters for the RF engineers to begin the verification simulations with zero learning curve. This follows the tradition of ease-of-use and instant productivity that Genesys is always sought after for.





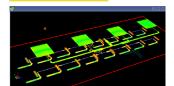
www.keysight.com/find/eesof-systemvue-lte-baseband-verification-library www.keysight.com/find/eesof-systemvue-wlan-library

# Genesys EM Simulation

The Genesys EM block includes Momentum, the most advanced 3D-planar electromagnetic simulator in the industry that is also used in Keysight EEsof ADS flagship EDA tool. Momentum enables you to analyze the EM effects of physical circuit layout components that were not in your original design schematic, such as microstrip interconnects for SMT components, solder pads, vias, proximity ground planes and covers. Tightly integrated circuit-EM cosimulation allows you to optimize circuit parameters to compensate for these parasitic effects before you construct hardware, thereby eliminating wasteful time-robbing iterations. Links to Keysight EMPro full 3DEM simulator and Sonnet provides automatic setup of ports, materials and structure for additional EM corroboration if needed.

#### EM building block

#### Capability module



#### Description

#### Momentum GXF

Highest-performance, integrated 3D-planar EM simulator in the industry, including fast multi-threaded simulation on multicore processors, polygonal mesher and highly memory-efficient NlogN solvers. Offers the highest speed and capacity for 3D-planar EM simulation to analyze complex multilayer layouts or large planar-antenna arrays. It is typically 25x faster than the previous Genesys EMpower simulator which it replaces and using far less memory than traditional rectangular-grid based EM solvers. Interactive 3D viewer for surface currents and antenna far-field opens up design insights and is indispensable for troubleshooting.

www.keysight.com/find/eesof-genesys-momentum



#### Keysight EMPro link

Keysight EMPro analyzes non-planar 3-D electromagnetic effects such as packaging, shielding and integration of circuit with waveguides. In a single click, Genesys exports its planar RF/microwave layout, along with ports and substrate material properties to EMPro for immediate simulation. Eliminates tedious manual re-entering of 3D structures, EM port locations and material properties.

www.keysight.com/find/eesof-empro



#### Sonnet link

Enables users of Sonnet planar EM simulator to take advantage of Genesys circuit/system synthesis and simulation by performing circuit-EM co-simulation.

www.keysight.com/find/eesof-genesys-sonnet

# Genesys Co-Simulation

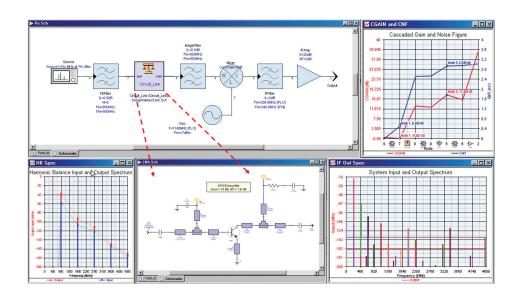
The already powerful system, circuit and electromagnetic simulators can also be used together to analyze, tune and optimize your designs in a single pass. This eliminates tedious and error-prone manual translation of data between simulators.

#### Circuit-system co-simulation

Circuit

System

Enables nonlinear circuit parameters to be tuned and optimized to system specs in one pass. It eliminates tedious, non-interactive and error-prone creation of inaccurate system behavioral models from circuits to perform circuit-system verification.



#### Circuit-EM co-simulation

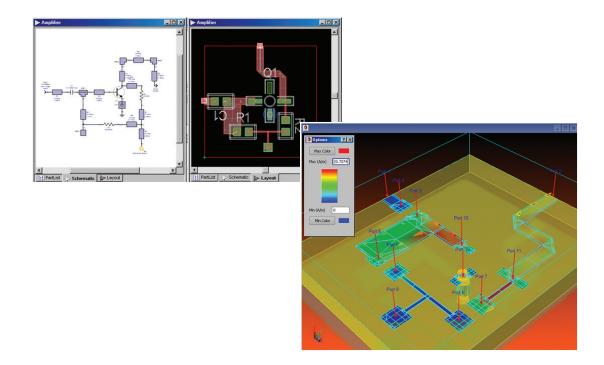
Circuit

EM

Automatically includes the physical effects of board layout in both linear and nonlinear circuit simulations.

It enables you to identify and fix circuit performance degradation caused by undesired proximity coupling, resonance and reflection from your RF board layout.

3D interactive viewing of animated surface current flows help you pinpoint the location of these problems without guesswork.



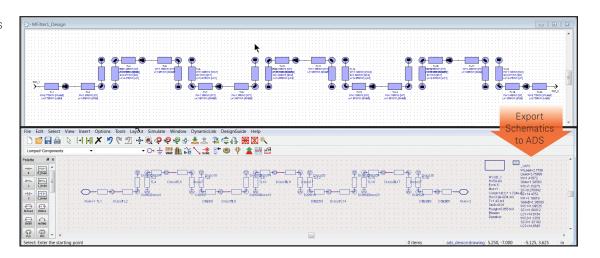
# Genesys For ADS Users

The automatic circuit synthesis and RF system architecture capabilities in Genesys are perfect complement to ADS users for enhancing personal design productivity at minimum cost.

#### Genesys synthesis to ADS schematic transfer

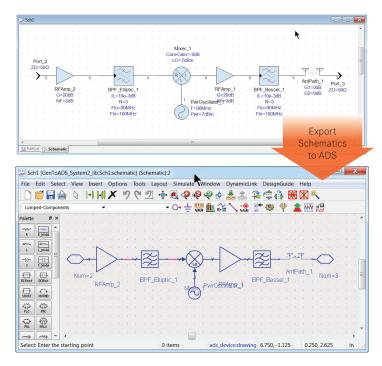
Using Genesys synthesis for broadband impedance matching network design reduces hours of tedious work into a few minutes of mouse clicks as you watch your matching network being synthesized and optimized automatically.

Designing demanding custom notched filters or difficult multiplexers are efficiently tackled with automatic filter synthesis and network transforms to produce novel realizable topologies within minutes instead of days of looking up references. The Genesys synthesized circuit schematic can be transferred to ADS with one click for inclusion into a bigger design.



#### Genesys system architecture to ADS schematic transfer

Quickly putting together a system block diagram and simulating it to pinpoint the components causing system impairments is one of the most valued capabilities in Genesys by all RF system designers. All of the above are now available to ADS users as the RF Architect and Synthesis elements W2362EP and W2372EP. They can also run as standalone Genesys bundles when available ADS licenses are depleted.



# Getting the Most Value From Your Keysight Genesys Software

Keysight has a worldwide network of trained professionals to help you be effective in using and deploying Genesys sooner. An annual software maintenance subscription is an affordable assurance that you will always be up-to-date with the latest software capabilities and bug fixes. It comes with highly-competent Keysight phone and email support, as well as unlimited 24/7 access to the Keysight Knowledge Center for solutions to thousands of engineering questions.

Training is recommended to bring all designers up to a similar skill level and to get the most out of Genesys for productive teamwork and organizational effectiveness.

If you have an old outdated Genesys, you can always upgrade it to the latest version to protect and enhance your original investment at minimum cost.

#### Support, training and upgrade

# Capability module Genesys software support option



#### Description

#### Software maintenance subscription and technical support

- Annual software maintenance subscription keeps your software fresh with the latest enhancements, applications, defect fixes, operating system, and hardware support.
- Provides access to the Keysight technical support network worldwide through email, telephone, and the 24/7 Keysight Knowledge Center.
- Software on current maintenance can be enhanced, upgraded to a floating license, re-hosted, or transitioned to Keysight ADS.
- Typically, there are about two software releases of Genesys per year.

#### www.keysight.com/find/eesof-support

#### N3244A



#### Genesys concepts - training class

Three-day, hands-on Genesys training class, updated for the current release. Can be delivered at Keysight training site (N3244A), or at your site (N3244B).

www.keysight.com/find/eesof-genesys-class

#### W1401R



#### Genesys software upgrade

If your Genesys is outdated, you can upgrade to the latest version at very low cost to preserve your investment in this valuable tool.

# Genesys Bundles

Genesys bundles are affordable combinations of useful capabilities that work seamlessly together at a fraction of the cost of any competing design tools. For ADS users, two elements containing Genesys automatic synthesis and system capabilities are specially created for boosting personal design productivity with one click schematic transfer. They can also run as standalone Genesys bundles.

Part	schematic transfer.	chematic transfer. They can also run as standalone Genesys bundles.								- EM			RF	RF	
Filter   F									System	EM	System	Modulated		System	System
March   Marc						System	Circuit	EM	Circuit	Circuit	Circuit	RF	System	Circuit	Circuit
Wiscolar   Wiscolar			FILTER MATCH	Synthesis	System	Synthesis	Synthesis	Synthesis	Synthesis	Synthesis	Synthesis	System	Synthesis	Synthesis	Synthesis
Genesys bundle name:   Canesys   Conesys   C															
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Cenesys care environment   Y	Genesys bundle name:		core, filter,	core,	core,	architect and synthesis	core, synthesis,	core,	core, synthesis, circuit,	core, synthesis,	core, synthesis, circuit,	core, system,	architect, synthesis, modulated-RF	synthesis, circuit, system,	synthesis, circuit, system, modulated-RF,
Testink															
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PLL Y Y Y Y Y Y Y Y Y Y Y Y Y Y Y Y Y Y	Equalize			Υ		Υ	Υ	Υ	Υ	Υ	Υ		Υ	Υ	Υ
Signal control	Oscillator			Υ		Υ	Υ	Υ	Υ	Υ	Υ		Υ	Υ	Υ
Mixer         Y <td>PLL</td> <td></td> <td></td> <td>Υ</td> <td></td> <td>Υ</td> <td>Υ</td> <td>Υ</td> <td>Υ</td> <td>Υ</td> <td>Υ</td> <td></td> <td>Υ</td> <td>Υ</td> <td>Υ</td>	PLL			Υ		Υ	Υ	Υ	Υ	Υ	Υ		Υ	Υ	Υ
Vendor Parts         Y <t< td=""><td>Signal control</td><td></td><td></td><td>Υ</td><td></td><td>Υ</td><td>Υ</td><td>Υ</td><td>Υ</td><td>Υ</td><td>Υ</td><td></td><td>Υ</td><td>Υ</td><td>Υ</td></t<>	Signal control			Υ		Υ	Υ	Υ	Υ	Υ	Υ		Υ	Υ	Υ
Circuit simulation           Harbec         Y	Mixer			Υ		Υ	Υ	Υ	Υ	Υ	Υ		Υ	Υ	Υ
Harbec         Y <td>Vendor Parts</td> <td></td> <td></td> <td>Υ</td> <td></td> <td>Υ</td> <td>Υ</td> <td>Υ</td> <td>Υ</td> <td>Υ</td> <td>Υ</td> <td></td> <td>Υ</td> <td>Υ</td> <td>Υ</td>	Vendor Parts			Υ		Υ	Υ	Υ	Υ	Υ	Υ		Υ	Υ	Υ
Cayenne         Y </td <td>Circuit simulation</td> <td></td>	Circuit simulation														
System simulation           Spectrasys         Y	Harbec						Υ		Υ	Υ	Υ			Υ	Υ
Spectrasys         Y	Cayenne						Υ		Υ	Υ	Υ			Υ	Υ
WhatIF         Y <td>System simulation</td> <td></td>	System simulation														
Modulated-RF           Dataflow simulator         Y	Spectrasys				Υ	Υ			Υ		Υ	Υ	Υ	Υ	Υ
Dataflow simulator         Y	WhatIF				Υ	Υ			Υ		Υ	Υ	Υ	Υ	Υ
Budget analysis         Y	Modulated-RF														
LTE, WLAN verification Y Y Y Y Y	Dataflow simulator											Υ	Υ	Υ	Υ
LTE, WLAN verification Y Y Y Y Y	Budget analysis												Υ	Υ	Υ
EM simulation												Υ	Υ	Υ	Υ
LIVI SIITUURUUH	EM simulation														
Momentum GXF Y Y Y Y	Momentum GXF							Υ		Υ	Υ				Υ
Sonnet link Y Y Y Y Y	Sonnet link							Υ		Υ	Υ				Υ

EM

Modulated

Modulated

# Genesys Licensing Options

License type	Node-locked	Networked
Perpetual license	<ul> <li>A node-locked perpetual license is locked to a USB key or PC LAN physical address</li> <li>You own the license and this is the most cost-effective option for the long term</li> <li>The node-locked perpetual license is the most popular option</li> </ul>	<ul> <li>The networked perpetual license enables convenient sharing by users across a network</li> <li>Costs about 30% more than node-locked perpetual license</li> <li>Suitable for larger companies and requires network license server administration</li> </ul>
Subscription (time-based) license	<ul> <li>The node-locked time-based license is locked to a USB key or PC LAN physical address</li> <li>Usage expires after the 12-months time-based license period</li> <li>Costs about 1/3 of a perpetual license including support; suitable for projects with tight cash flows</li> </ul>	<ul> <li>The networked time-based license enables convenient sharing by users across a network</li> <li>Costs about 30% more than a node-locked time-based license</li> <li>Suitable for larger companies who need to optimize cash flows</li> <li>Requires network license server administration</li> </ul>

# Securing Your Genesys License

Genesys licenses can be secured to your personal computer's (PC) local area network (LAN) physical address or a universal serial bus (USB) hardware key serial number.

LAN Physical Address - When secured to your computer LAN physical address, the danger of losing the small USB hardware key is eliminated and your license enables you to start using Genesys just like any other software on your PC.

USB Hardware Key - When secured to a USB hardware key, you have the convenience of license portability to run Genesys on different computers. However, you may risk losing the USB key which then prevents you from using Genesys.

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